

# ENGINEERING AND TECHNOLOGY GROUP

## ELECTRONICS TECHNOLOGY

### Scheme of Examination

#### Std. XI

Paper	Title of the Paper	Theory		Practical		Term work	Project work	I.V. *	Total Marks
		Marks	Time (Hrs)	Marks	Time (Hrs)				
2	Basic Electronics	80	3	80	3	20	10	10	200

\* IV = Industrial Visits

### Paper II : Basic Electronics Theory

Sr. No.	Unit	Sub-Unit	Periods
1.	Semiconductors	1.1 Atomic structure	13
		1.2 Semiconducting material	
2.	Special semiconductor diodes	2.1 P-N Junction	13
		2.1 Special Diodes	
3.	Power Supplies	3.1 Rectifiers	27
		3.2 Filters	
		3.3 Characteristics	
		3.4 Regulators	
		3.5 UPS	
		3.6 Inverter	
4.	Amplifiers	4.1 Transistor	27
		4.2 Amplifiers	
		4.3 Multistage amplifiers	
		4.4 Feed backs in Amplifier	
		4.5 Differential Amplifier	
5.	Oscillators	5.1 Basic of oscillator	20
		5.2 LC oscillator	
		5.3 RC oscillator	
		5.4 Crystal	
6.	Special Semiconductor devices	6.1 FET	20
		6.2 UJT	
		6.3 SCR, Diac, Triac	
<b>Total Periods</b>			<b>120</b>